



Thank you for purchasing FlashLink AX. Before applying this module, please confirm the [Package Contents], and read the users manual. An update document of FlashLink including data sheet and application note is placed on URL below. Please download the necessary document.

[URL] <http://www.dgway.com/products/FlashLink>

[Package Contents]

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|---|------------------------------|
| ● FlashLink module | 1pcs. |
| ● Label (sticker) with the user ID and password printed | 1pcs (Labeled on this sheet) |
| ● Users manual and Q&A | 1sheet (This sheet) |

Q&A

Q1: I can't find a download site of FlashLink software.

A1: Confirm the typing URL spell. <http://www.dgway.com/FlashLink> F and L are capital letters. The download requires the user ID and password that are labeled on this sheet.

Q2: The FlashLink software can not recognize connected FlashLink module.

A2: Check each item listed below.

- (1) Check that ByteBlasterMV or ParallelCable3 is properly connected to the parallel port of the PC. "ByteBlaster" without "MV" type is not available because it does not support +3.3V power voltage. Also, some ParallelCable3 that does not use shottkey diode internally can't be available.
- (2) Check that ByteBlasterMV is correctly connected with the programming connector of FlashLink. The 1PIN side of ByteBlasterMV should attach with the LED side of FlashLink. For ParallelCable3, refer to the data sheet for the connection of Flying Lead with programming connector of FlashLink.
- (3) Check that FlashLink is correctly inserted to the Altera Passive-Serial/Xilinx Slave-Serial configuration connector on the user board.
- (4) Check that the user board supplies +3.3V power voltage to FlashLink. FlashLink only accepts +3.3V power voltage.
- (5) If the FlashLink software is operating under Windows2000/XP environment, check that dedicated device driver "IoDrvr.sys" is properly installed.



- Q3: Configuration is completed and LED turns on green light, but the user board does not operate.
A3: Check each item listed below.
- (1) Check that connector on user board for FlashLink is **not the Jtag connector but the Altera Passive-Serial of Xilinx Slave-Serial configuration connector**.
 - (2) Finish the FlashLink software, and execute Altera-PS configuration by Max+Plus2 or Quartus with ByteBlasterMV directly, or execute Xilinx-SS configuration by Device Programming tool with ParallelCable3 directly. If Host-PC configuration by Altera/Xilinx software completes successfully, inquire at info@dgway.com.
- Q4: The configuration failed and LED turns on red light.
A4: Check each item listed below.
- (1) Confirm that the programmed chain settings completely match with the user board.
 - (2) The configuration data possibly did not programmed correctly because the previous program operation might be failed or cancelled. Execute verify operation and check data.
 - (3) Finish the FlashLink software, and execute Altera-PS configuration by Max+Plus2 or Quartus with ByteBlasterMV directly, or execute Xilinx-SS configuration by Device Programming tool with ParallelCable3 directly. If Host-PC configuration by Altera/Xilinx software completes successfully, inquire at info@dgway.com.
- Q5: When FlashLink is mounted, the user board could not fit to a rack slot because of a module height.
A5: Use optional FLA-CABLE. When FlashLink is mounted via this cable, board height can be limited.
- Q6: While program or verify operation is in progress, data communication error often occurs.
A6: Check each item listed below.
- (1) Check that software that accesses parallel port (GuardKey access by Max+Plus2 or Quartus for example) does not work active. Such software should stay idle state when the FlashLink software communicates with FlashLink via parallel port.
 - (2) Check that device drivers that access parallel port are not installed. Some device drivers periodically scan parallel port and such device drivers might interrupt FlashLink communication.
 - (3) If FlashLink software is operating under Windows2000/XP OS environment, a Plug&Play function of OS scans parallel port in a few minutes after OS detects FlashLink. This scan action occurs only after the first detection of FlashLink since PC is powered up. In such case, wait a couple of minutes during this OS scan action.
- Q7: How to execute Altera/Xilinx mixed configuration?
A7: Refer to the application note [FL-AN01: User board design for FlashLink]. This note can be downloaded from Design Gateway web site.
If the mixed configuration is the first trial, build user board circuit according to the prototype board design guideline in application note so that direct PC configuration for individual Altera/Xilinx can be executed.