

IP Portfolio & Design Service

There is a range of existing IPs that can be customized or ported to your needs. Design Gateway cores have been extensively exercised and are used in working applications, and is bundled with complete soft core (VHDL source code), test benches, documentation, hardware evaluation board, standard license agreement and technical support

Design Gateway also provides custom electronic design services in the areas of computer hardware and IP cores (VHDL/Verilog), FPGA to ASIC conversion and Design service.

The cores are delivered with a complete set of tools, including:

- Customization guideline
- Testbench or Test environment in VHDL/Verilog
- Constraint files
- design examples
- Various test software
- Technical support from our experienced team

Bus interface cores

- PCI Interface (Master/Target)
- USB 1.1/2.0 Device Controller **Underdevelopment**
- I2C Controller (Single-Master/Multi-Master/Slave)
- UART (with/without FIFO)

Memory controller

- SDRAM Controller (66MHz, 100MHz, 133MHz)
- DDRAM Controller (133MHz) **Underdevelopment**

Network interface

- MAC Controller **Underdevelopment**

Microprocessor & Microcontroller

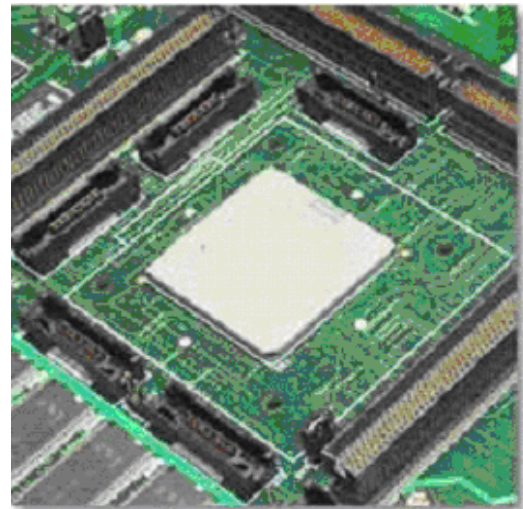
- 8-bit RISC Microcontroller core

Encryption

- DES Cryptoprocessor
- 3DES Triple-DES Cryptoprocessor

Peripheral Controller

- ATA Device controller (ATA33/66/100/133)
- Programmable Timer/Counter
- Programmable Interrupt Controller



Design Flow

