

Thank you for purchasing JtagLink. Before applying this module, please confirm the Package Contents and read the user manual. An update document of JtagLink is placed on URL below.

[URL] <http://www.dgway.com/products/JtagLink>

[Package Contents]

- |   |                               |
|---|-------------------------------|
| ● JtagLink module                                       | 1 pc.                         |
| ● Label (sticker) with the user ID and password printed | 1 pc. (Labeled on this sheet) |
| ● User manual and Q&A                                   | 1 sheet. (This sheet)         |
| ● Registration Card                                     | 1 sheet.                      |

## Q&A

Q1: I can't find a download site of JtagLink software.

A1: Confirm the typing URL spell. <http://www.dgway.com/JtagLink> J and L are capital letters. The download requires the user ID and password that are labeled on this sheet.

Q2: The JtagLink software can not recognize connected JtagLink module.

A2: Check each item listed below.

- (1) Check that ByteBlaster(BB)MV or BB2, or ParallelCable(PC)3 or PC4 is properly connected to the parallel port of the PC. It does not support the cable which is not supported 3.3V and USB cable (MasterBlaster/MultiLynx).
- (2) Check that download cable is correctly connected with the Host connector of JtagLink. Refer to the datasheet [JL-DS01] for the connection of the cable and JtagLink.
- (3) Check that JtagLink is correctly inserted to the user board. Refer to the datasheet [JL-DS01] for the connection with the user board.
- (4) Check that the user board supplies +3.3V power voltage to JtagLink. JtagLink only accepts +3.3V power voltage.
- (5) If the JtagLink software is operating under Windows2000/XP environment, check that dedicated device driver "IoDrvr.sys" is properly installed.

Q3: The configuration failed and LED turns on red light.

A3: Check each item listed below.

- (1) Confirm that the programmed chain settings completely match with the user board by menu [PC Check]-[Check JTAG] on the software.
- (2) Check that configuration data is correctly by menu [PC Check]-[PC Config] on the software.
- (3) Check that wiring pattern of TCK or TMS signal is appropriate as JTAG signal on the user board.
- (4) Check that written data is correctly by executing menu [Program]-[Verify].
- (5) Finish the JtagLink software, and execute configuration directly by Quartus or iMPACT. If Host-PC configuration by Altera/Xilinx software completes successfully, inquire at [info@design-gateway.com](mailto:info@design-gateway.com).

Q4: When JtagLink is mounted, the user board could not fit to a rack slot because of a module height.

A4: Use optional FLA-CABLE. When JtagLink is mounted via this cable, board height can be limited.

Q5: While program or verify operation is in progress, data communication error often occurs.

A5: Check each item listed below.

- (1) Do not execute software that accesses parallel port (Quartus/iMPACT etc.) during writing/verify/erase execution to JtagLink by JtagLink Software. Also do not execute software that accesses GuardKey of parallel port.
- (2) Check that device drivers that access parallel port are not installed. Some device drivers periodically scan parallel port and such device drivers might interrupt JtagLink communication.
- (3) If JtagLink software is operating under Windows2000/XP OS environment, a Plug&Play function of OS scans parallel port in a few minutes after OS detects JtagLink. This scan action occurs only after the first detection of JtagLink since PC is powered up. In such case, wait a couple of minutes during this OS scan action.

## Warranty Condition

1. Product warranty is valid within 6 months from purchasing date.
2. Warranty is void if any modification has been made to this product and any incorrect operation from this manual.