



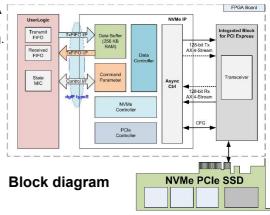


Evaluation on KCU105 with Samsung SSD 960 Pro

Directly connect PCIe SSD without external memory!!

NVMe IP core interfaces Ultra high-speed PCIe SSD without CPU and external memory. It is the best solution for applications which require ultra high-speed performance with compact system. The IP core license includes the reference design for Xilinx FPGA boards to shorten development time and reduce the cost.

Free evaluation bit files for Xilinx FPGA boards are available. You can evaluate IP core performance before purchasing.

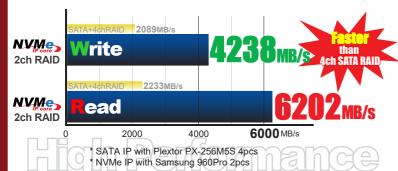


Features

- Implement application layer to access PCIe SSD without CPU and external memory
- Support PCIe Gen3, theoretical upper limit 4GB/sec
- Small resource, the best solution for building a compact system
- FAT32 access without CPU * with optional FAT32-IP
- Support PLDA PCIe Gen3 Soft IP
- Free evaluation before purchasing

<u>Rerformance</u>, <u>Application</u>

Able to build Gen3 PCIe SSD 2ch RAID system!!



Product,Line.up

IP core

11 0010	
NVMe-IP-VUP	1 project Netlist License for Virtex®UltraScale+
NVMe-IP-ZUP	1 project Netlist License for Zynq [®] UltraScale+
NVMe-IP-KU	1 project Netlist License for Kintex [®] UltraScale
NVMe-IP-VT7	1 project Netlist License for Virtex [®] 7
NVMe-IP-KT7	1 project Netlist License for Kintex-7
NVMe-IP-AT7	1 project Netlist License for Artix ² 7
NVMe-IP-ZQ7	1 project Netlist License for Zynq ² 7000
NVMe-IP-FAT32-X	FAT32 file system for NVMe-IP. Purchase with NVMe-IP core
Please ask us about Multi-License, Evaluation License and Maintenance support License.	

Reference Designs are available for practical applications



Easy to apply for high-end products such as ultra high-speed data recorder



Suitable for high-speed data recording and stand-alone data analysis on SoC



Kintex-7 (FBG484) NVMe IP+ user logic

M.2 SSD for data storage

System space image by 484pin FBG package FPGA with M.2 SSD

Accessories for evaluation



IP core Evaluation Demo are available on youtube

Design Gateway Co.,Ltd.

