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dg\_pcieahciip\_instruction\_en.doc

# PCIe AHCI-IP Demo Instruction

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## PCIe AHCI-IP Demo Instruction

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This document describes the instruction to show PCIeSSD demo by using SATA AHCI-IP, SATA-IP, and PCIeIP connecting with SATA-III/II SSD on Xilinx evaluation kit (VC707/KC705). Through 4-lane PCIe @ Gen2 speed, PC running Fedora21 OS with specific device driver can detect the board to be SCSI device. So, user can write/read data to the board by using general application like typical SATA-III/II device. This demo uses new test application (diskTestApp) to check disk performance.

#### **1** Hardware Requirement

As shown in Figure 2, to run PCIe AHCI-IP demo please prepare

- 1) Xilinx Evaluation board (VC707/KC705)
- 2) PC Power adapter included in Xilinx evaluation kit



Figure 1 PC Power adapter

- 3) Micro USB cable for JTAG programming
- 4) iMPACT ver 14.4 or later to program bit file to the board through JTAG
- 5) AB09-FMCRAID board, provided by Design Gateway
- 6) 2.5-inch SATA-III/II Device or other size with adapter cable
- 7) PC which has available 8-lane PCIe Gen2 and install Fedora21 OS (Linux kernel version 3.18).

<u>Note</u>: 8-lane PCIe is required to match with PCIe connector size on FPGA board though only 4-lane is used in the design.





Figure 2 PCIe AHCI-IP Demo Environment Setup on VC707/KC705



## 2 Hardware setup

- Power off board and PC
- Connect FMC SATA RAID board to FMC1\_HPC connector (J35) on VC707 or FMC\_HPC connector (J22) on KC705
- Note: FMC SATA RAID board is provided by Design Gateway.
- Connect PC power cable to power connector on AB09-FMCARID board for SATA-III/II device power
- Connect 2.5-inch SATA-III/II Device to CN0 on AB09-FMCRAID board
- Set DIPSW bit [2:1] at SW2 for VC707/SW11 for KC705 to select SATA speed mode. DIPSW Description is shown in Table 1



### Figure 3 DIPSW to select SATA speed mode

DIPSW[2]	DIPSW[1]	Description
<b>'1'</b>	'1'	Fixed-speed at SATA3 (6.0Gbps)
'1'	'0'	Fixed-speed at SATA2 (3.0Gbps)
'0'	'X'	Auto-speed negotiation mode
Table 1 DIPSW setting description		

- Connect USB micro B cable from U26 for VC707/U59 for KC705 to USB Port on PC for JTAG programming
- Connect PC power adapter, provided in Xilinx evaluation kit, between PC power cable and Xilinx evaluation kit

<u>Warning</u>: Do not use the PCIe connector from the PC power supply to connect to FPGA board





Figure 4 Board power connection through adapter (not direct connection from PC)

- Insert VC707/KC705 board into PC's 8-lane PCIe Gen2 slot
- Turn-on Power switch on FPGA board, and then power up PC
- Open iMPACT and download bit file to VC707/KC705 board.

😵 ISE iMPACT (P.20131013) - [Boundary Scan]				
File Edit View Operations	<u>O</u> utput Debug <u>W</u> indow <u>H</u> elp	_ 8 ×		
🗋 ờ 🛃 🕺 🛍 🍒 🎛	部 🗄 🕾 🖪 🥕 🚱			
IMPACT Flows       ↔       □       □       ×         Image: SystemACE       Image: SystemACE       Image: SystemACE       Image: SystemACE         Image: Image: SystemACE       Image: SystemACE       Image: SystemACE       Image: SystemACE       Image: SystemACE         Image: Image: SystemACE       Image: SystemACE	TDI xc7vx485t pcie_ahci.bit TDO	* E		
	Boundary Scan			
Console		⇔⊡₽×		
'1': Programmed success	sfully.	4   V 		
Errors 🔬 Warnings 📃 Co	Configuration Digilent JTAG-SMT1	1000000		

#### Figure 5 FPGA programming by iMPACT



 Check GPIO LEDs status on VC707/KC705 board at LED0-LED1. Both LEDs must be ON, as shown in Figure 6. LED2 status depends on connecting SATA device speed. Each LED description is described as follows.

<u>Note</u>: To access hardware register at BAR0 area, the design supports only 1 DW size access. If more than 1 DW is accessed, LED4/LED5 will be ON to show error status.

LED	ON	OFF
LED0	OK	150 MHz of SATA clock on FMC SATA RAID cannot lock.
		Please check 150 MHz clock source on FMC SATA RAID
		board.
LED1	OK	SATA-IP cannot detect SATA device.
		Please check SATA device and the connection.
LED2	Linkup at SATA-III speed	Linkup at SATA-II speed
LED3	SATA in operating	No SATA operating
LED4	Unsupported PCIe write	No error
	access to BAR0 area	
LED5	Unsupported PCIe read	No error
	access to BAR0 area	

Table 2 LED Status of PCIeAHCI reference design



Figure 6 LED status after system set up complete when linkup at SATA-3 speed

• Restart PC to send soft reset and restart PCIe enumeration and configuration. Then, PC can detect the new device.



#### 3 Linux Setup

- Create working directory to store driver and test application file (dg\_PCleAHCl.tar.gz) which can be downloaded from DesignGateway website. In this demo, assumed that working directory is "Home/dg PCIeAHCI". Then, extract the file. Three files are provided to run the demo, i.e.
  - dg libahci.ko -
- : Common AHCI SATA low-level routines
- dg\_PCIeAHCI.ko
- : AHCI SATA platform driver
- diskTestApp -
- : Disk Test application to check performance



Change current directory to working directory. •



#### Figure 8 Change current directory



- To insert module, root permission is required. Type "su" to change permission as root.
  Type "insmod dg\_libahci.ko" and "insmod dg\_PCIeAHCI.ko" sequentially to insert module, as shown in Figure 9.

pond@localhost:/home/pond/dg_PCIeAHCI/driver_dgPCIeAHCI ×
File Edit View Search Terminal Help
[pond@localhost driver_dgPCIeAHCI]\$ su Password: Password:
[root@localhost driver_dgPCIeAHCI]# insmod dg_libahci.ko [root@localhost driver_dgPCIeAHCI]# insmod dg_PCIeAHCI.ko
[root@localhost driver_dgPCleAHCl]# [root@localhost driver dgPCleAHCl]# dmesg -c
[  265.884112] dg_pcie_ahci 0000:01:00.0: version 3.0 [  265.884347] dg_pcie_ahci 0000:01:00.0: irq 32 for MSI/MSI-X [  265.884402] dg_pcie_ahci 0000:01:00.0: AHCI 0001.0300 32 slots 1 ports 6 Gbps 0x1 impl unkn
own mode
[ 265.884409] dg_pcie_ahci 0000:01:00.0: flags: ncq only
[ 265.887108] scsi host7: ahci
[ 265.887382] ata8: SATA max UDMA/133 abar m262144@0xf7d00000 port 0xf7d00100 irq 32
[ 266.192070] ata8: SATA link up 6.0 Gbps (SStatus 133 SControl 300)
[ 266.214127] ata8.00: supports DRM functions and may not be fully accessible
[ 266.234322] ata8.00: failed to get NCQ Send/Recv Log Emask 0x1
[ 266.234325] ata8.00: ATA-9: Samsung SSD 850 PRO 256GB, EXM01B6Q, max UDMA/133
[ 266.234327] ata8.00: 500118192 sectors, multi 1: LBA48 NCQ (depth 31/32), AA
[ 266.274838] ata8.00: supports DRM functions and may not be fully accessible
[ 266.295054] ata8.00: failed to get NCQ Send/Recv Log Emask 0x1
[ 266.305195] ata8.00: configured for UDMA/133
[ 266.305427] scsi 7:0:0:0: Direct-Access ATA Samsung SSD 850 1B6Q PQ: 0 ANSI: 5
[ 266.305702] sd 7:0:0:0: [sdb] 500118192 512-byte logical blocks: (256 GB/238 GiB)
[ 266.306027] sd 7:0:0:0: [sdb] Write Protect is off
[ 266.306030] sd 7:0:0:0: [sdb] Mode Sense: 00 3a 00 00
[ 266.306043] sd 7:0:0:0: [sdb] Write cache: enabled, read cache: enabled, doesn't support DP
0 or FUA
[ 266.306100] sd 7:0:0:0: Attached scsi generic sgl type 0
[ 266.317008] sdb: sdb1 SATA Device Information
[ 266.317395] sd 7:0:0:0: [sdb] Attached SCSI disk
[root@localhost driver_dgPCIeAHCI]#

Figure 9 Insert Module



## 4 Example Disk Command

#### 4.1 Create Disk Partition

As shown in Figure 10, type "fdisk /dev/sdb" to call the tool to start disk management. In the example, new partition (sdb1) is created.

[root@localhost driver_dgPCIeAHCI]# fdisk /dev/sdb -> Call the tool to manage disk partition
Welcome to fdisk (util-linux 2.25.2). Changes will remain in memory only, until you decide to write them. Be careful before using the write command.
Device does not contain a recognized partition table. Created a new DOS disklabel with disk identifier 0x36495134.
Command (m for help): n -> Create new partition Partition type p primary (0 primary, 0 extended, 4 free) e extended (container for logical partitions) Select (default p): p Partition number (1-4, default 1): 1 First sector (2048-500118191, default 2048): Last sector, +sectors or +size{K,M,G,T,P} (2048-500118191, default 500118191):
Created a new partition 1 of type 'Linux' and of size 238.5 GiB.
Command (m for help): w -> Write table to the disk The partition table has been altered. Calling ioctl() to re-read partition table. Syncing disks.
[root@localhost driver_dgPCIeAHCI]#

Figure 10 Create Disk Partition



#### 4.2 Format Disk

To format the disk, user needs to select file system type such as FAT, EXT4. This example shows only the command to format to EXT4 by typing following command. >> mkfs.ext4 /dev/sdb1

#### Figure 11 Format Disk

#### 4.3 Mount Disk

Before running any application to access the disk such as Test Application or Bonnie++, disk must be mounted firstly by following command. >> mount /dev/sdb1 /mnt

[root@localhost	driver_dgPCIeAHCI	# mount	/dev/sdb1	/mnt
[root@localhost	driver_dgPCIeAHCI	#		

Figure 12 Mount Disk



## 5 Performance Test by Test Application

Test application provided by DesignGateway is used in the demo to show disk performance.

[root@localhost dg_PCIeAHCI]# ./diskTestApp diskTestApp version 1.1
Usage: ./diskTestApp [OPTION]
Options:
-r For raw data test
-f PATH For file system test
[root@localhost dg_PCIeAHCI]#
Figure 13 diskTestApp usage

As shown in Figure 13, test application can run in two data formats, i.e. raw data or file system. More details to run test application in each format are described as follows. *Warning: If running raw data test, file system in that disk partition will be lost.* 

#### 5.1 Run Test Application in Raw Mode

Before run the application, user needs to log in as root. Type "./diskTestApp –r" to run the test application in raw data format. Five input parameters are required in test application, i.e. 1) Disk selection to select the disk to test performance.

- 1) Disk selection to select the disk to test performance
- 2) Operation type: '0'-Read disk test, '1'-Write disk test
- 3) Test pattern:
  - '0': dummy test data for write/no data verification for read
  - '1': 32-bit increment test data for write/verify by 32-bit increment data for read
  - 2': 32-bit decrement test data for write/verify by 32-bit decrement data for read
- 4) Disk offset: Disk start address in sector unit to run write/read data test. 0x prefix is added to input in hex unit while default value without prefix is decimal unit.
- 5) Operation length: Transfer length in sector unit to run write/read data test. 0x prefix is added to input in hex unit while default value without prefix is decimal unit.

Figure 13 and Figure 14 show the example of write test while Figure 16 and Figure 17 show the example of read test. Dummy test data will show higher test performance than 32-bit increment pattern for both write and read test because no CPU resource is required to fill or verify test data in dummy mode.











#### 5.2 Run Test Application in File System Mode

Before run the Application, user needs to log in as root. The disk must have file system to run the test in this mode. Type "./diskTestApp –f <directory>" to run the test in file system format. Five input parameters are required to run test application, i.e.

- 1) File name input: File name to run the test
- 2) Operation type: '0'-Read file test, '1'-Write file test
- 3) Test pattern:
  - '0': dummy test data for write/no data verification for read
  - '1': 32-bit increment test data for write/verify by 32-bit increment data for read
  - '2': 32-bit decrement test data for write/verify by 32-bit decrement data for read
- 4) File number: Total number of files to run write/read data test
- 5) File size: Size of one file in sector unit to run write/read data test

Similar to raw data test, dummy mode will show higher performance than 32-bit increment pattern for both write and read test file, as shown in Figure 18 - Figure 21.







```
Base of filename (default TEST, hit "Ctrl+c" to exit): TEST
Operation type (read(0)/write(1), default 0):0
Pattern type (none(0)/inc(1)/dec(2), default 0):0
File number (1-100, default 1): 1
// *** *** *** *** *** //
// Your operation.
Operation Type: Read, Pattern Type: None
FileBaseName: TEST, fileNum: 1, fileSize: 0x00000000 00000000
File: /mnt/TEST 00.bin
[OK] Reading completed
                              Read test without data verification
        @speed = 511.03 MB/s
// Test on /mnt mounted point
Base of filename (default TEST, hit "Ctrl+c" to exit):
        Figure 20 Read File Test without data verification by 32 GB size
Base of filename (default TEST, hit "Ctrl+c" to exit): TEST
Operation type (read(0)/write(1), default 0): 0
Pattern type (none(0)/inc(1)/dec(2), default 0): 1
File number (1-100, default 1): 1
// *** *** *** *** *** *** //
// Your operation.
Operation Type: Read, Pattern Type: Increment
FileBaseName: TEST, fileNum: 1, fileSize: 0x00000000_00000000
File: /mnt/TEST 00.bin
[OK] Reading verification completed
        @speed = 306.47 MB/s
                            //Read test with increment data verification
// Test on /mnt mounted point
Base of filename (default TEST, hit "Ctrl+c" to exit):
         Figure 21 Read File Test with data verification by 32 GB size
```



#### 5.3 Performance test by Bonnie++ Software

Please see more details about Bonnie++ user manual from <u>http://linux.die.net/man/8/bonnie</u>++.

The example of test result when running by Bonnie++ is shown in Figure 22.

[root@localhost dg_PCIeAHCI]#	bonnie++ -d /mnt -s	15712M -n 0 -m test	-f -u root
Using uid:0, gid:0.			
Writing intelligentlydone			
Rewritingdone			
Reading intelligentlydone			
start 'emdonedonedon	edonedone		
Version 1.96Seq Concurrency 1 -Per Chr- Machine Size K/sec %CP test 15712M Latency	uential Output BlockRewrite- K/sec %CP K/sec %CP 486461 26 121750 56360us 275ms	Sequential Input- -Per ChrBlock K/sec %CP K/sec %CP 9 235140 4760us	Random- Seeks /sec %CP 10 6695 109 4085us
1.96,1.96,test,1,1436427961,1 ,,,,,,,,,,,,,56360us,275ms,,4 [root@localhost dg_PCIeAHCI]#	5712M,,,,486461,26,1 760us,4085us,,,,,,	21750,9,,,235140,10,6	5695,109,,,,,,
Figure 22	Test result when runn	ing by Bonnie++	



## 6 Revision History

Revision	Date	Description
1.0	10-Jul-15	Initial version release